

Notice of References Cited		Application/Control No. 10/803,323	Applicant(s)/Patent Under Reexamination HSU ET AL.	
		Examiner Mark Connolly	Art Unit 2115	Page 1 of 1

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*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
*	A	US-2004/0255170	12-2004	Yang et al.	713/300
*	B	US-2003/0204686	10-2003	Hesse et al.	711/154
*	C	US-6,732,280	05-2004	Cheok et al.	713/300
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	M	US-			

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NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Advanced Micro Devices, HyperTransport Technology I/O Link: A High-Bandwidth I/O Architecture, 20 July 2001, pgs. 1-25.
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*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
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